START

Architecture of the CPU

CPU Performance

Embedded Systems

1.1 End of topic assessment

STOP

1.21 Intervention

Primary storage

Secondary Storage

Units

Wired and wireless networks, protocols and layers

Networks and topologies

1.2 Intervention

1.2 End of topic assessment

Compression

Data storage

1.3 End of topic assessment

STOP

1.3 Intervention

Threats to computer systems and networks

Identifying vulnerabilities

1.4 End of topic assessment

STOP

1.4 Intervention

Operating systems

Utility software

Paper 1 mock exam

1.6 Intervention

1.6 End of topic assessment

STOP

Ethical, Legal, Cultural, Environmental impact

1.5 Intervention

1.5 End of topic assessment

STOP